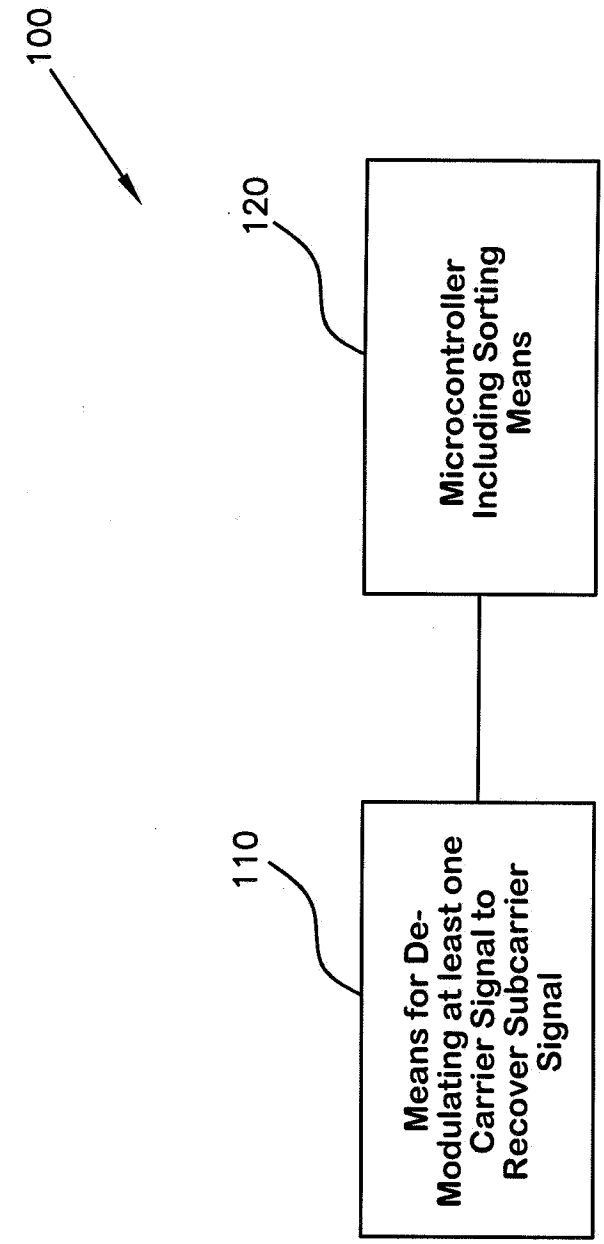
100
120
Microcontroller Including Sorting Means
110
Means for De-Modulating at least one Carrier Signal to Recover Subcarrier Signal

FIG.1

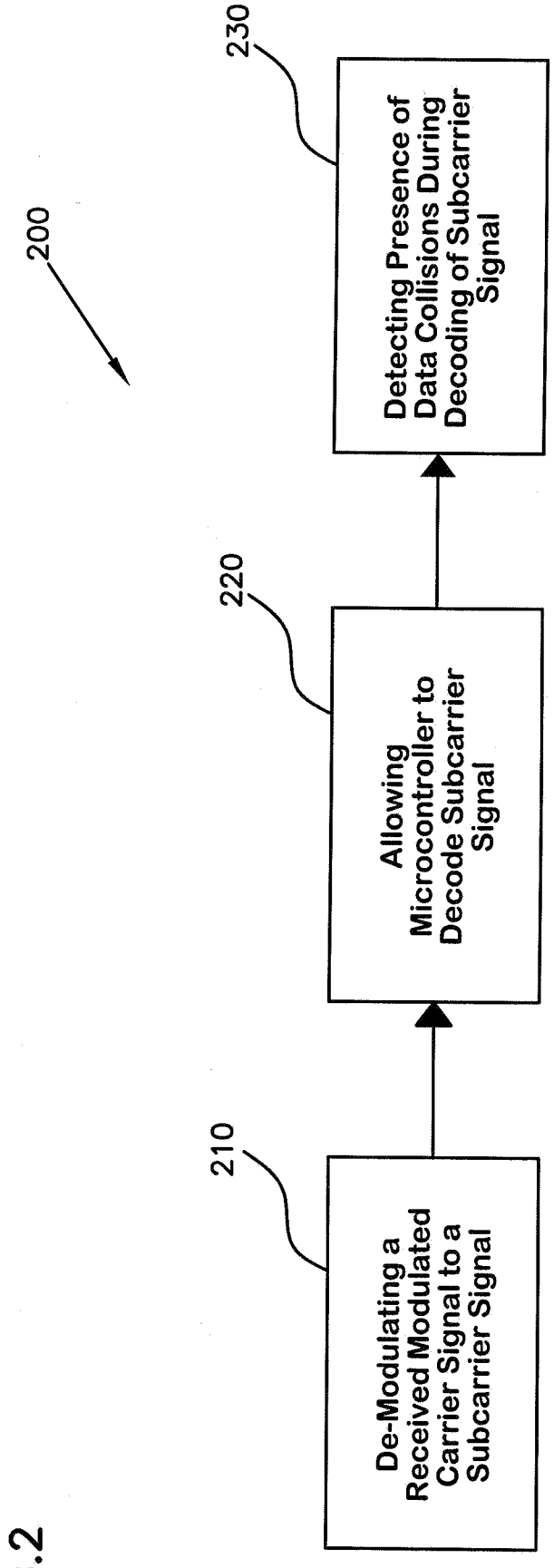
200
210
De-Modulating a Received Modulated Carrier Signal to a Subcarrier Signal
220
Allowing Microcontroller to Decode Subcarrier Signal
230
Detecting Presence of Data Collisions During Decoding of Subcarrier Signal

FIG.2

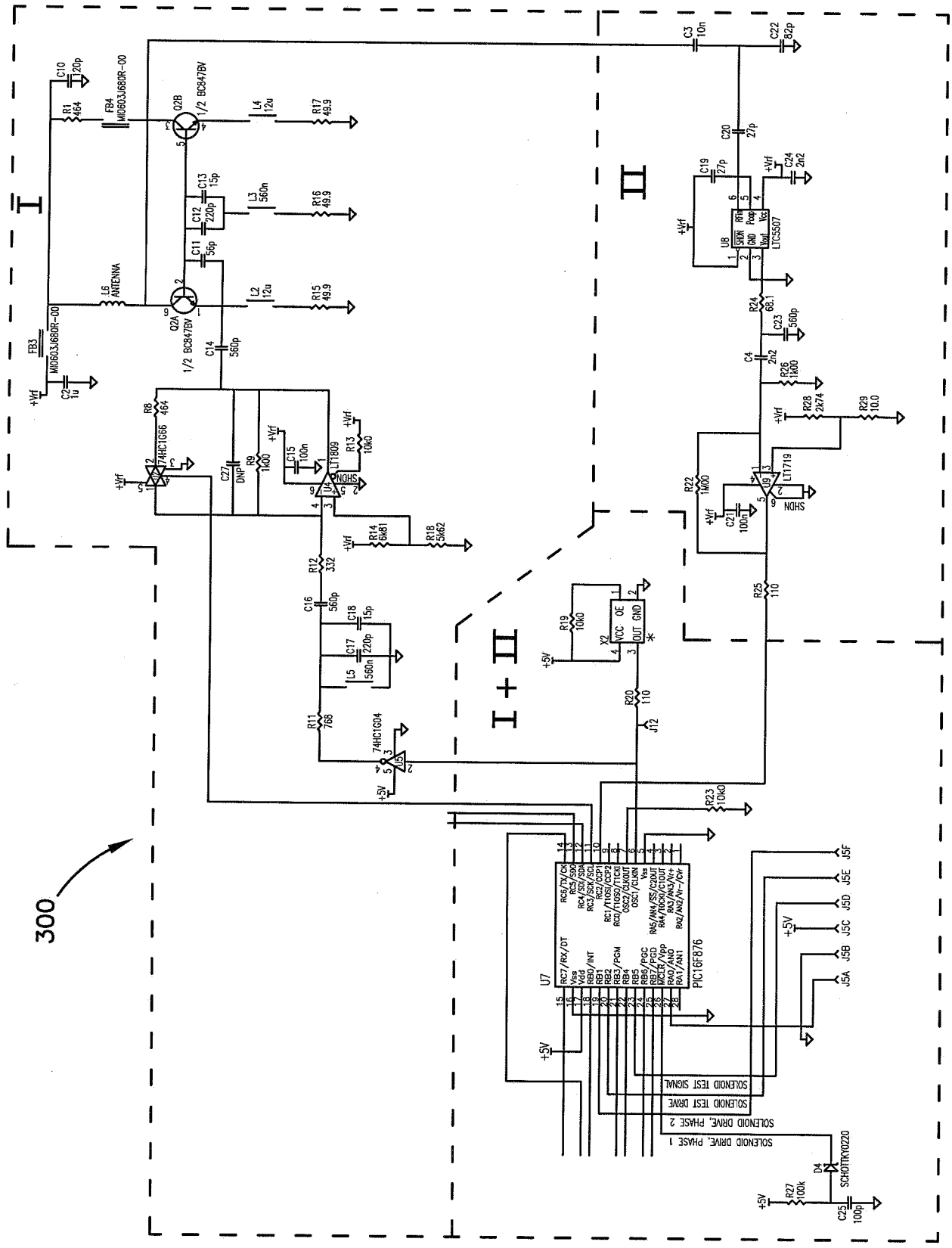
I
II
I+II
300
C10 20p
R1 464
FB4 MI0603J680R-00
Q2B 1/2 BC847BV
L4 12u
R17 49.9
C13 15p
C12 220p
C11 56p
L3 560n
R16 49.9
L6 ANTENNA
Q2A 1/2 BC847BV
L2 12u
R15 49.9
FB3 MI0603J680R-00
C14 560p
+Vrf
C2 1u
R8 464
74HC1G66
C27 DNP
R9 1k00
C15 100n
LT1609
R13 10k0
SHDN
R14 6k81
R18 3k62
R12 332
C16 560p
C18 15p
C17 220p
L5 560n
R11 768
74HC1G04
+5V
C3 10n
C22 82p
C20 27p
C19 27p
C24 2n2
U8 LTC5507
R24 68.1
C23 560p
C4 2n2
R26 1k00
R28 2k74
R29 10.0
U9 LT1719
R22 1k00
C21 100n
R25 110
R19 10k0
X2
R20 110
J12
R23 10k0
U7 PIC16F876
J5F
J5E
J5D
J5C
J5B
J5A
SOLENOID TEST SIGNAL
SOLENOID TEST DRIVE
SOLENOID DRIVE, PHASE 2
SOLENOID DRIVE, PHASE 1
D4 SCHOTTKY0220
R27 100k
C25 100p

FIG.3

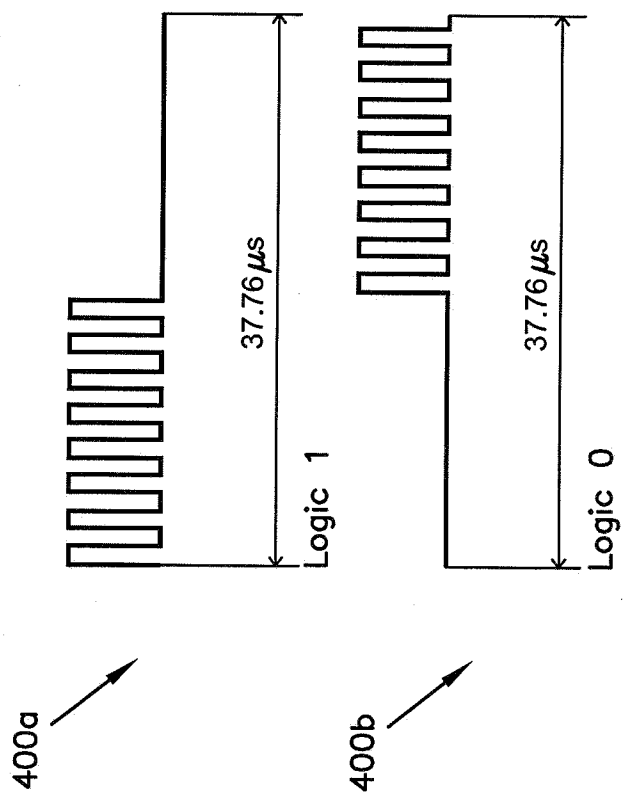

FIG.4
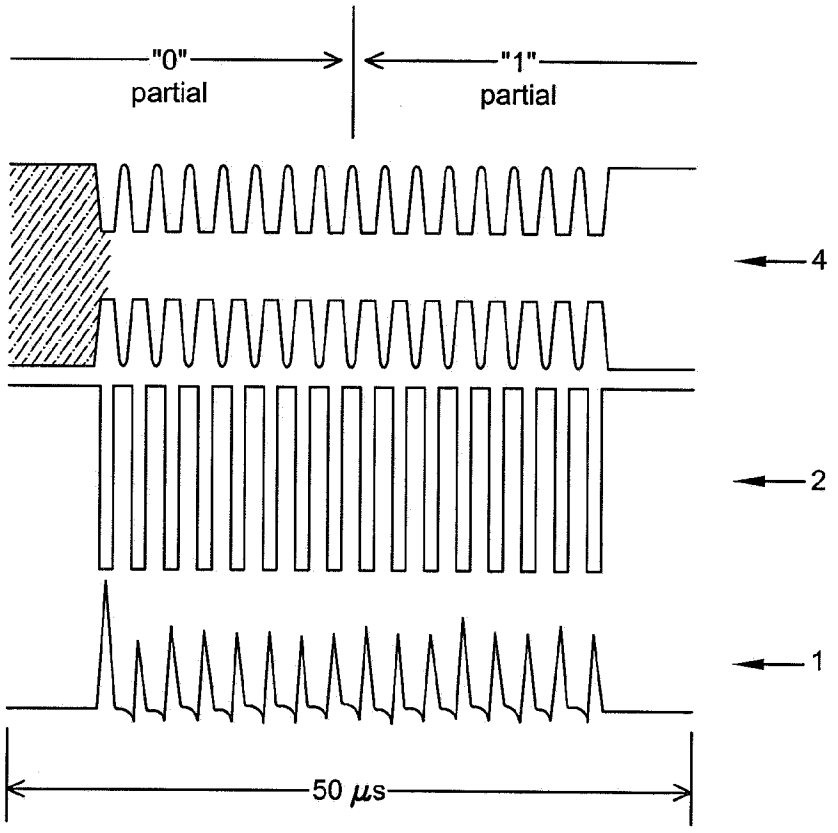
FIG.5